

*H1 cancel*  
said division number setting means setting the division number so that M transport packets are divided into N sync blocks to form the recording format;

header appending means for generating a first header for each of the M transport packets and appending the first header to each of the M transport packets; and

format forming means for forming N consecutive sync blocks from the data after the division of the bit stream.

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Please add the following new claims:

--27. (New) A digital VTR for magnetically recording and replaying digitally transmitted bit stream in a predetermined recording format, comprising:

*H2 could*  
an address control circuit, responsive to a bit stream input including a predetermined number M of transport packets as a unit, wherein M is an integer, wherein N sync blocks are related to the transport packets such that N is not equal to M, and wherein N is an integer;

said address control circuit setting the division number so that M transport packets are divided into N sync blocks to form the recording format;

a header appending circuit for generating a first header for each of the M transport packets and appending the first header to each of the M transport packets; and

a track format circuit for forming N consecutive sync blocks from the data after the division of the bit stream.

28. (New) A digital VTR for magnetically recording and replaying a digitally transmitted bit stream in a predetermined recording format, comprising:

a data identifying circuit for decoding header information of the input bit stream;

a data extraction circuit for extracting, from the input bit stream, a series of encoded data of image blocks used for fast replay, based on the decoded header information;

a decoder for decoding the series of coded data of the input bit stream and for outputting a transformation coefficient belonging to the decoded data;

a coefficient counter for counting the number of transformation coefficients; and

a data amount control circuit for receiving the coefficient count number from the coefficient counter in such a way that the data length of the extracted, coded data of an integer number of image blocks is reduced to a data amount which can be recorded in K sync blocks in said predetermined format, wherein K is a positive integer.

29. (New) A digital VTR as set forth in claim 6, wherein said header appending means also appends a sync block header to each of said N sync blocks.

30. (New) A digital VTR as set forth in claim 27, wherein said header appending circuit also appends a sync block header to each of said N sync blocks.--